

## DR. BABASAHEB AMBEDKAR TECHNOLOGICAL UNIVERSITY, LONERE

## End Semester Examination – DEC 2018

	Branch: B. Tech Subject Name: Electronic devices and Circuits Subject Code: B	
	05/12/2019 Duration:- 3 1	Hrs.
	Max Marks: 00	
	<ol> <li>Instructions to the Students:         <ol> <li>Each Question Carries 12 Marks</li> <li>Attempt any five questions of the following</li> <li>Illustrate your answers with neat sketches, diagram etc., wherever necessa</li> <li>If some part or parameter is noticed to be missing, you may appropriately and should mention it clearly</li> </ol> </li> </ol>	ary y assume it
Q. 1	Attempt Any Two questions of the following	06
	a) Briefly Explain construction and working principle of JFE1	-
	b) Describe FET as an Amplifier and its Analysis(CS)	06
	c) Explain about FET as High Impedance Circuits	06
Q.2	a) Briefly Explain the Characteristics of N-Channel E-MOSFET	06
	b) Write short Notes on Non-Ideal Voltage current characteristics	06
Q.3	a) Explain the Classification of amplifier in details	06
	b) Discuss in details about various feedback topologies	06
Q.4	Attempt Any Two questions of the following	
	a) Explain about Barkhausen criterion for oscillator	0
	b) Draw and explain the RC Phase shift Oscillators	0
	c) Describe the Hartley and colpitts oscillators	0
Q.f	a) Briefly Explain about the IC 555 Block Diagram	(
	b) Write down the applications of IC 555 in Engineering	(
Q.	6 Attempt Any Two questions of the following	
	a) Explain about adjustable three terminal positive (LM 317) regulators with	
	neat diagram b)Briefly Explain SMPS with Neat diagram	
	c) Write Down the Comparison of Linear power supply and SMPS	
	c) Write Down the Companison of Emour power in	